

<b>Notice of References Cited</b>	Application/Control No. 10/804,873	Applicant(s)/Patent Under Reexamination HOFMANN ET AL.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

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*	C	US-3,766,526	10-1973	Buchanan, James A.	710/51
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*	E	US-5,701,516	12-1997	Cheng et al.	710/22
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"PRESTOR-1 A Processor Extending Multithreaded Architecture" by Tanaka, K. (abstract only) □ □ Publication Date: 17-19 Jan. 2005
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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